

PTO/SB/08A (07-05)

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PATENT &amp; TRADEMARK OFFICE

**Complete if Known****INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Application Number	10/796,274
Filing Date	03/10/2004
First Named Inventor	Paul Rich et al.
Art Unit	2823
Examiner Name	M. Estrada
Attorney Docket Number	WLJ.103

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A	US- 5725740	03/10/1998	Raaijmakers	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

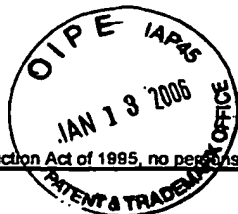
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First Named Inventor	Paul Rich et al.
Art Unit	2823
Examiner Name	Michelle Estrada
Attorney Docket Number	WLJ.103

Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
ME	B	"Applied Materials Introduces New PVD Tungsten System for Sub-100nm Transistor Gate Manufacturing", BizWire, Decemver 4, 2002	✓
ME	C	Y. Hara et al., "Low Resistivity Mo-W Alloy for A-Si TFT Gate Electrode", Met. Res. So. Symp. Porc. Vol. 403, 1996 Materials Research Society, pages 675-680.	✓

Examiner Signature	Michelle Estrada	Date Considered	9/25/00
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Sheet 1

of 1

**Complete if Known**

Application Number	10/796,274
Filing Date	01/13/2006
First Named Inventor	Paul Rich et al.
Art Unit	2823
Examiner Name	Michelle Estrada
Attorney Docket Number	WLJ.103

## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
Pae AM	D	KR2003001744	01/08/2003	Hynix Semiconductor Inc		✓
	E	EP 0598422 A1	10/06/1993	Philips Electronics N.V.		✓

**Examiner  
Signature**

Date  
Considered

9/25/04

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